

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	27821	circuit near5 layout	US-PGPU B; USPAT; USOCR	OR	ON	2006/09/01 12:26
L2	449	1 and (sub-transistor (sub near3 transistor))	US-PGPU B; USPAT; USOCR	OR	ON	2006/09/01 12:26
L3	2088	1 and (sub-transistor sub-circuit (sub near5 circuit) (sub near3 transistor))	US-PGPU B; USPAT; USOCR	OR	ON	2006/09/01 12:27
L4	908	3 and (column and row)	US-PGPU B; USPAT; USOCR	OR	ON	2006/09/01 12:06
L5	47	4 and ("16" sixteen) near10 (sub-transistor sub-circuit (sub near5 circuit) (sub near3 transistor)))	US-PGPU B; USPAT; USOCR	OR	ON	2006/09/01 12:11
L6	7607	circuit near5 layout	FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/09/01 12:26
L7	8	6 and (sub-transistor (sub near3 transistor))	FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/09/01 12:27
L8	87	6 and (sub-transistor sub-circuit (sub near5 circuit) (sub near3 transistor))	FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/09/01 12:27